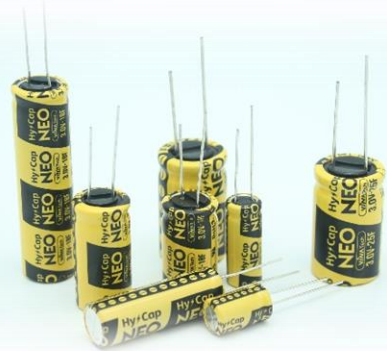


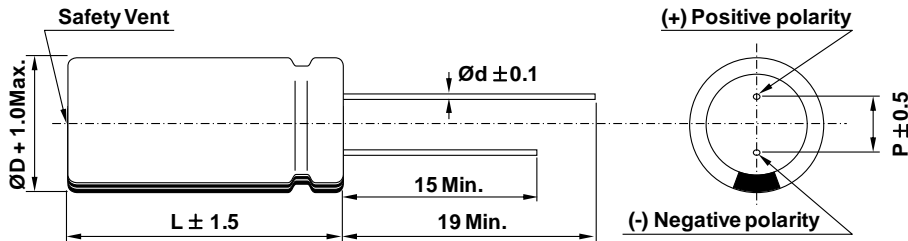
## FEATURES

### EDLC (Electric Double Layer Capacitor)

- High Power Density (Low ESR)
- Over 500,000 cycle life (semi-permanent)
- Higher energy density compared with 2.7V Caps
- RoHS compliant
- Long-term reliability improved at high temperature and humidity(65°C 90%RH)



## Drawing



D(Φ)	8	10, 13	16, 18
d(Φ)		0.6	0.8
P(mm)	3.5	5.0	7.5

## SPECIFICATION

ITEM	CHARACTERISTICS
Product series	EDLC
Rated Voltage (V <sub>R</sub> )	3.0V
Operating Temperature	-40 ~ +65°C
Capacitance Tolerance	-10 ~ +30%
High Temperature Load Life	After 1,000 hours at V <sub>R</sub> loaded under +65°C, capacitors meet the following criteria. Capacitance Change ≤ 30% of initial value ESR ≤ 2 times of specified value
Cycle Life	Cycle Over 500,000 ΔC ≤ 30% of initial value
Characteristics	ESR ≤ 2 times of specified value
Method	Cycle of Charge/discharge from V <sub>R</sub> to 1/2V <sub>R</sub>
Shelf Life	2 Years No Electrical Charge, Temperature below 70°C (ΔC : ≤ 10% of initial value / ΔESR : ≤ 50% of specified value)

# 3.0V NEO SERIES - Lead terminal



Part Number	Rated Voltage (V)	Capacitance (F)	ESR (mΩ)		Max. Current (A)	Leakage Current (mA, 72hr)	Size (mm) D × L	Weight (g)	Volume (ml)
			AC(1kHz)	DC					
WEC3R0105QG	3.0	1	175	265	1.0	0.003	08 × 13	1.1	0.7
WEC3R0155QG		1.5	115	175	1.5	0.005	08 × 20	1.4	1.0
WEC3R0205QG		2	115	175	2.0	0.006	08 × 20	1.4	1.0
WEC3R0335QG		3.3	85	145	3.0	0.010	08 × 20	1.5	1.0
WEC3R0505QD		5	50	85	5.0	0.015	08 × 25	1.8	1.3
WEC3R0705QD		7	45	75	6.5	0.021	08 × 30	2.2	1.5
WEC3R0505QG		5	80	120	4.5	0.015	10 × 20	2.1	1.6
WEC3R0705QG		7	80	135	5.0	0.021	10 × 20	2.2	1.6
WEC3R0106QA		10	45	75	8.5	0.030	10 × 25	2.6	2.0
WEC3R0106QG		10	35	55	9.5	0.030	10 × 30	3.2	2.4
WEC3R0106QD		10	50	75	8.5	0.030	13 × 20	3.4	2.7
WEC3R0156QG		15	37	55	12.0	0.045	13 × 25	4.5	3.3
WEC3R0186QC		18	30	50	14.0	0.054	13 × 25	4.8	3.3
WEC3R0256QG		25	25	40	18.5	0.075	16 × 25	7.2	5.0
WEC3R0506QG		50	15	22	35.0	0.150	18 × 40	12.5	10.2
WEC3R0606QG		60	15	22	38.0	0.180	18 × 40	13.5	10.2

\* **Max. Current** : 1 sec. discharge to  $1/2V_R$

\* **When do module more than 2 series, please fully discharge over 1 hour first, then assemble right after within 1 hour.**